

ABSTRACT

A CPU (28) determines, based on a signal from a power source determination circuit (30), whether or not power is supplied from an external power source. When power is supplied from an external power source, the CPU (28) controls the USB interface (16) to operate as a ver. 2.0 interface. When power is not supplied from an external power source, the CPU (28) controls the USB interface (16) to operate as a ver. 1.1 an interface, thereby reducing power consumption.